

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): S. Brandon Keller, et al.

Confirmation No.:

Application N .: Not Assign

Examiner:

Filing Date: Herewith

Group Art Unit:

Title: System And Method For Processing Configuration Information

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e)(1) or (2), and  
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).  
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00 . At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. EV210655533US

Date of Deposit Jan. 30, 2004

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: Wendy Precht

Respectfully submitted,

S. Brandon Keller, et al.

By

Peter C. Knops

Attorney/Agent for Applicant(s)  
Reg. No. 37,659

Date: Jan. 30, 2004

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200310172-1

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## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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## FOREIGN PATENT DOCUMENTS

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	1L					
	1M					
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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,596, Entitled: System And Method Analyzing Design Elements In Computer Aided Design Tools; Filed August 25, 2003
1R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,608, Entitled: System And Method For Determining Unmatched Design Elements In A Computer-Automated Design; Filed August 25, 2003
1S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,606; Entitled: System And Method For Determining Connectivity Of Nets In A Hierarchical Circuit Design; Filed August 25, 2003

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2Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,598, Entitled: Computer Aided Design System And Methods With Reduced Memory Utilization; Filed August 25, 2003
2R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,688, Entitled: System And Method For Iteratively Traversing A Hierarchical Circuit Design; Filed August 25, 2003
2S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,769, Entitled: Systems And Methods For Establishing Data Model Consistency Of Computer Aided Design Tools; Filed August 25, 2003

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3Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,594, Entitled: Systems And Methods For Determining Activity Factors Of A Circuit Design; Filed August 25, 2003
3R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,687, Entitled: Systems And Methods Utilizing Fast Analysis Information During Detailed Analysis Of A Circuit Design; Filed August 25, 2003
3S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,595, Entitled: System And Method For Determining Applicable Configuration Information For Use In Analysis Of A Computer Aided Design; Filed August 25, 2003

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4Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,607, Entitled: Systems And Methods For Identifying Data Sources Associated With A Circuit Design; Filed August 25, 2003
4R	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,605, Entitled: Systems And Methods For Performing Circuit Analysis On A Circuit Design; Filed August 25, 2003
4S	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,768, Entitled: System And Method For Determining A Highest Level Signal Name In A Hierarchical VLSI Design; Filed August 25, 2003

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6Q	✓	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,376, Entitled: Method And Program For Visual Display and One-Click Repair Of Electromigration And Self Heating Design-Rule Violations In A Digital Integrated Circuit Layout Database; Filed November 12, 2003
6R	✓	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,501, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit By Converting A Netlist To A DC Model And Performing DC Analysis Of The DC Model; Filed November 12, 2003
6S	✓	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,526, Entitled: Method And Program Product For Performing A Worst Case Electromigration And Self Heating Analysis In A Digital Integrated Circuit With Worst-Case Superposition Of Partial Currents; Filed November 12, 2003

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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	7Q	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,692, Entitled: Method And Program Product For Performing Electromigration Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003
	7R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,508, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Layout Database by Substituting Resistive Models For Active Devices; Filed November 12, 2003
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5Q	KELLER, S. Brandon; ROGERS, Dennis R.; ROBBERT, George H.; U.S. Patent Application Serial Number 10/647,597, Entitled: System And Method For Determining Wire Capacitance For A VLSI Circuit; Filed August 25, 2003
5R	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,682, Entitled: Method And Program Product For Determining Nets Requiring Detailed Electromigration And Self Heating Analysis In A Digital Integrated Circuit; Filed November 12, 2003
5S	KELLER, S. Brandon; ROGERS, Dennis R.; LELM, Charles A.; U.S. Patent Application Serial Number 10/706,698, Entitled: Method And Program Product For Performing Self-Heating Analysis In A Digital Integrated Circuit Through A Single Cycle Transient Simulation; Filed November 12, 2003

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